

(19)



JAPANESE PATENT OFFICE

PATENT ABSTRACTS OF JAPAN

(11) Publication number: **63156236 A**

(43) Date of publication of application: **29.06.88**

(51) Int. Cl.

**G06F 9/42**  
**G06F 9/34**

(21) Application number: **61303443**

(22) Date of filing: **19.12.86**

(71) Applicant: **TOSHIBA CORP**

(72) Inventor: **MAEDA KENICHI**  
**SAITO MITSUO**  
**AIKAWA TAKESHI**  
**MATOKA TSUKASA**  
**OKAMURA MITSUYOSHI**

**(54) REGISTER DEVICE**

**(57) Abstract:**

**PURPOSE:** To quickly perform processing by switching plural register sets consisting of plural registers to not only momentarily perform saving and restoring operations but also hold the contents of registers before saving.

**CONSTITUTION:** When a switching control circuit 6 selects a register set 1a, writing of write data WD in a register specified with a register number AD is possible and the processing using the set 1a is performed. If a subroutine is called in this main routine, the circuit 6 switches the register set 1a to a register set 1b. Then, data of the set 1a is saved. If a second subroutine is called, the register set 1b is switched to a register set 1c similarly. The contents of specific registers in the preceding register set can be referred to, based on write flags WF of write flag registers 5aW5c after switching and the processing is quickly performed.

**COPYRIGHT:** (C)1988,JPO&Japio

